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S2	183	(stor\$3 or sav\$3) with (image or file or data) with (un\$1partitioned or un\$1formatted or un\$1allocated) with (space or area or region)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 13:54
S3	46	S2 and sector with (offset or location or number)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 14:06
S4	2	S3 and install\$5 with (operating near system or OS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 14:06
S5	1098	711/173.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 14:05
S6	148	S5 and sector with (offset or location or number)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 14:22
S7	27	S6 and install\$5 with (operating near system or OS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 14:23
S8	373	S5 and physical with (location or address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 14:22
S9	24	S8 and install\$5 with (operating near system or OS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 14:23

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S10	3	("6385766" or "6543047" or "6041395").pn.	USPAT	OR	ON	2007/06/08 15:15
S11	816	717/174-178.ccls.	USPAT	OR	ON	2007/06/08 15:37
S12	110	S11 and (operating near system or OS) and partition\$3	USPAT	OR	ON	2007/06/08 15:37
S13	79	S12 and @ad<"20010430"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 15:38
S14	2	(S5 or S11) and (sector and image and \$3partition\$3 and (operating near system or OS) and install\$5).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/08 16:28


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Guillermo A. Alvarez, Walter A. Burkhard, Flaviu Cristian

 May 1997 **ACM SIGARCH Computer Architecture News , Proceedings of the 24th annual international symposium on Computer architecture ISCA '97**, Volume 25 Issue 2

Publisher: ACM Press

Full text available: pdf(1.50 MB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

### 2 Decoupled sectored caches: conciliating low tag implementation cost



A. Seznec

 April 1994 **ACM SIGARCH Computer Architecture News , Proceedings of the 21ST annual international symposium on Computer architecture ISCA '94**, Volume 22 Issue 2

Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(1.06 MB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

### 3 Don't use the page number, but a pointer to it



André Seznec

 May 1996 **ACM SIGARCH Computer Architecture News , Proceedings of the 23rd annual international symposium on Computer architecture ISCA '96**, Volume 24 Issue 2

Publisher: ACM Press

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Greg Humphreys, Matthew Eldridge, Ian Buck, Gordan Stoll, Matthew Everett, Pat Hanrahan  
 August 2001 **Proceedings of the 28th annual conference on Computer graphics and interactive techniques SIGGRAPH '01**

Publisher: ACM Press

 Full text available: [pdf\(333.39 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

### 2 [System-level power optimization: techniques and tools](#)

Luca Benini, Giovanni de Micheli  
 April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2

Publisher: ACM Press

 Full text available: [pdf\(385.22 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

### 3 [Multicore software: "MAMA!": a memory allocator for multithreaded architectures](#)

Simon Kahan, Petr Konecny  
 March 2006 **Proceedings of the eleventh ACM SIGPLAN symposium on Principles and practice of parallel programming PPOPP '06**

Publisher: ACM Press

 Full text available: [pdf\(155.00 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

### 4 [Kernel korner: IBM's journaled filesystem](#)

Steve Best, David Gordon, Ibrahim Haddad  
 January 2003 **Linux Journal**, Volume 2003 Issue 105

Publisher: Specialized Systems Consultants, Inc.

 Full text available: [html\(20.45 KB\)](#) Additional Information: [full citation](#), [index terms](#)

### 5 [Code placement for improving dynamic branch prediction accuracy](#)

Daniel A. Jiménez  
 June 2005 **ACM SIGPLAN Notices , Proceedings of the 2005 ACM SIGPLAN conference on Programming language design and implementation PLDI '05**, Volume 40 Issue 6

Publisher: ACM Press

 Full text available: [pdf\(144.82 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)